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... Figure 6 identifies **OUTN** and **OUTP** for the differential output pins driven by Q8
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VGA001 - **Variable Gain Amplifier**
... LVSS. Ground. IREF25u. Reference Current. CNTRL. **Gain** Control Voltage. **INP**. Positive
Input. **INN**. Negative Input. **OUTP**. Positive Output. **OUTN**. Negative Output. ...
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... **Inp Inn Outn Outp** Fig. ... Iinp Qinp Qinn Iinn **outn outp** C R1 R2 Following the limiter ...
consists of a channel selection LPF and a **variable gain amplifier** (VGA) with ...
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... VAROUTR 32 O **Variable** output for right channel audio ... MAX UNIT | VOS | Class-D Output
offset voltage (measured differentially) **INN** and **INP** connected together ...
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United States Patent Application: 0010033191
... 2. Voltages comp(V.sub.**INP**) and comp(V.sub.**INN**) are the ... The voltages across the two
capacitors, comp(v.sub.**OUTP**) and comp(v.sub.**OUTN**), are the output ...
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1. VGA001 - **Variable Gain Amplifier** 🗗
   100Hz - 100MHz **Variable Gain Amplifier**. Fully differential input and output. Can be used as single-ended input **OUTN** ...
   amisemiconductor.com/ tech_resources/tech_docs/featuresheets/vga001 - 25k - Cached - More from this site

2. An Integrated Receiver Channel for Picosecond Range Laser Radar Pulses (PDF) 🗗
   ... mode **gain** control cell, a transimpedance. preamplifier, a voltage **amplifier and** ... **outp**. i. **outn**. i. **inn**. i. **inp** ...
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May 2004
...consists of a **variable-gain amplifier** (VGA) and
an...synthesizer, a **variable-gain** range of over...identifies
OUTN and **OUTP** for the differential...observe that **INN** and
INP are internal...inputs from the **variable-gain** IF **amplifier**
and that LON...
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☐ **2.** No Title
Dec 1996
...130 5.4 **Gain**-of-Two Buffer...two open-loop T/Hs. (b) Open-
loop T/H **gain** and output common-mode volt- age is
servoed...which constitute wiring capacitance, **amplifier**
input/output capacitance, and resistor-asso...
[http://www.icsl.ucla.edu/aagroup/PDF_files/ppai-thsis....]
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☐ **3.** Integrated electronic and optoelectronic circuits and devices for
pulsed time-of-flight laser rangefinding
Mar 2003
...fraction discriminator CMGC current mode **gain** control CMOS
complementary MOS, a semiconductor...capacitor-resistor CW
continuous wave EGC electronic **gain** control ESD electrostatic
discharge FM...converter TOF time of flight VMGC voltage mode
gain control A voltage **gain** c speed of light...
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☐ **4.** App Notes > WIRELESS, RF, AND CABLE
Sep 2003
...consists of a **variable-gain amplifier** (VGA) and
an...synthesizer, a **variable-gain** range of over...identifies
OUTN and **OUTP** for the differential...observe that **INN** and
INP are internal...inputs from the **variable-gain** IF **amplifier**
and that LON...

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outn "variable gain"
amplifier
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☐ **5.** MIGAS-Slides-Abidi.pdf
Nov 2002.
...capacitor techniques. · A **gain** of 40dB for A2 is enough
to...the 3rd condition through the **gain** of the VGA's and switch
to...Combining PGA (Programmable **Gain Amplifier**) & VGA
(Continuously **Variable Gain Amplifier**): · 64-QAM: Close
points in...
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☐ **6.** MAX2309/MAX2312 at 190MHz IF for W-CDMA
Jun 2003
...consists of a **variable-gain amplifier** (VGA) and
an...synthesizer, a **variable-gain** range of over...identifies
OUTN and **OUTP** for the differential...observe that **INN** and
INP are internal...inputs from the **variable-gain** IF **amplifier**
and that LON...
[http://www.maxim-
ic.com/appnotes.cfm/appnote_number/38...]
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☐ **7.** Design of CMOS Wideband SC Bandpass Filters
Aug 2003
...sampling frequency of 176MHz. A 3-step **variable gain** is
designed with each step being...measured to be 27dBm at a
voltage **gain** of 3dB. The proposed filter consumes...32 3.5
Implementation of Selective **Gain** 37 3.6 System Simulation 38
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1. ☐ **A physical-based noise macromodel for fast simulation of switching noise generation** •
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   Microelectronics Journal, Volume 35, Issue 8, August 2004, Pages 677-684
   Luis Elvira, Ferran Martorell, Xavier Aragonés and José Luis González
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